

1c598 U.S. PTO
09/536452

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Class	Subclass
ISSUE CLASSIFICATION	

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U.S. UTILITY Patent Application

O.I.P.E. K2W SCANNED <i>AA</i> Q.A. <i>Ne</i>	PATENT DATE
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APPLICATION NO. 09/536452	CONT/PRIOR	CLASS 712	SUBCLASS 209	ART UNIT 2183	EXAMINER <i>AN Haisman</i>
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APPLICANTS

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Alexander Peleg

TITLE

Confining an application to a 32-bit address space subset in a 64-bit processor

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.			CLAIMS ALLOWED Total Claims Print Claim for O.G.	
	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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